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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Caroll, Chris; Cheng, Yeou-Yen; Meyer, Jon; Reysen, Bill; Twu, Bor-Long; Vanniasinkam, Joseph; Viray, Chris; Wai, John

Assignee: Unassigned

Title: Filter Based Multiplexer/Demultiplexer Component

Serial No.: 09/808,197 Filing Date: March 13, 2001

Examiner: Unknown Group Art Unit: Unknown

Docket No.: M-9341 US

San Jose, California
August 7, 2001

Attn: Official Draftsperson
COMMISSIONER FOR PATENTS
Washington, D. C. 20231

SUBMISSION OF FORMAL DRAWINGS

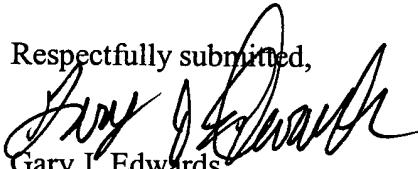
Dear Sir:

Applicant(s) submit(s) sixteen (16) sheets of formal drawings, consisting of Figures 1A, 1B, 1C, 2A, 2B, 2C, 3A, 3B, 3C, 4A, 4B, 4C, 5A, 5B, 5C, 5D, 5E, 5F, 5G, 5H, 5I, 5J, 5K, 5L, 5M, 5N, 5O, 5P, 5Q, 5R, 6, 7A, 7B, 7C, and 7D, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (408) 453-9200.

EXPRESS MAIL LABEL NO:

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Respectfully submitted,


Gary J. Edwards

Attorney for Applicant(s)
Reg. No. 41,008

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FIG. 1A Prior Art

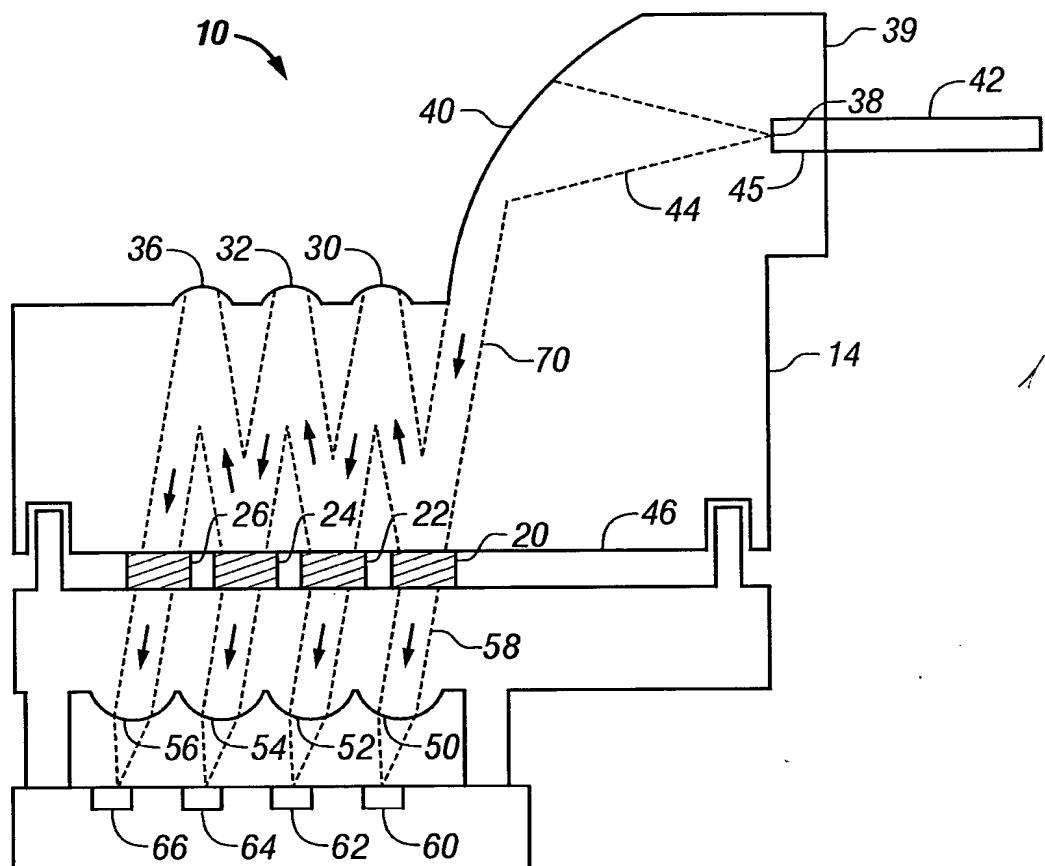


FIG. 1A
(Prior Art)

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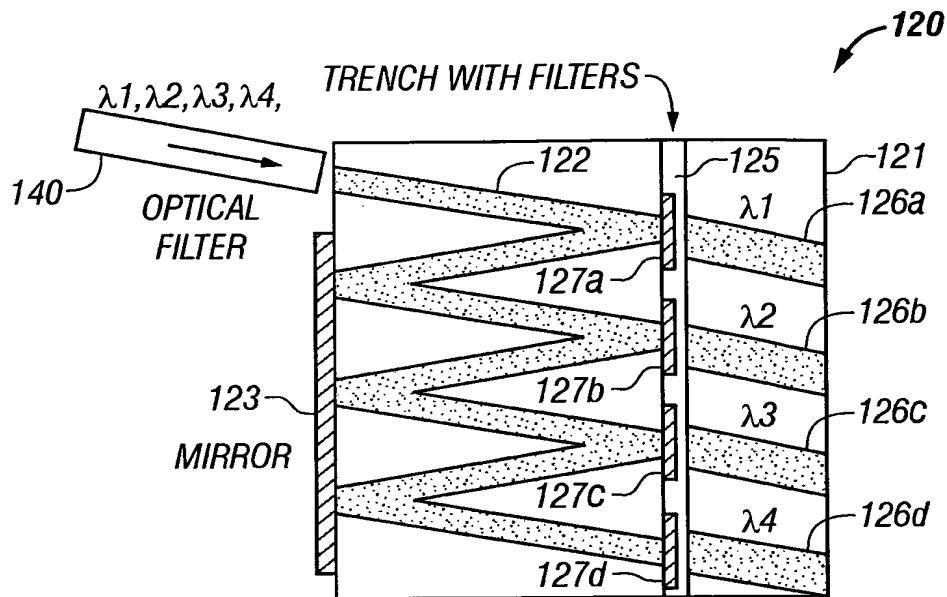


FIG. 1B

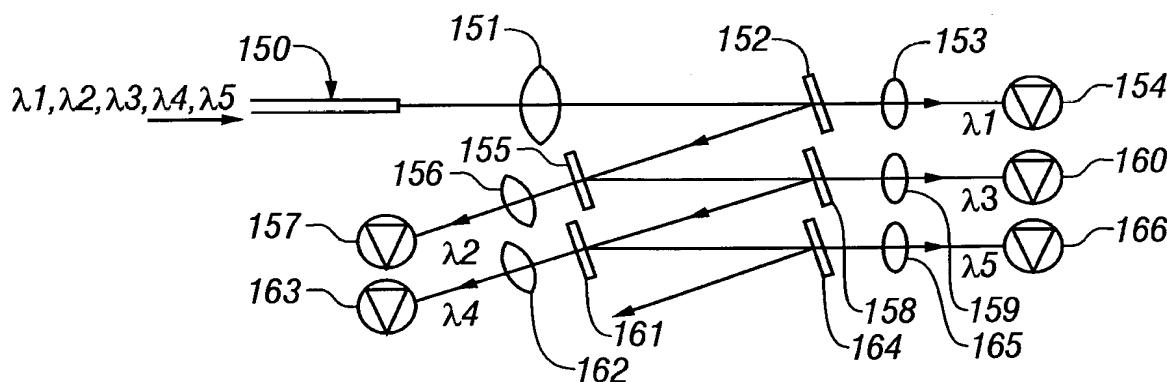


FIG. 1C

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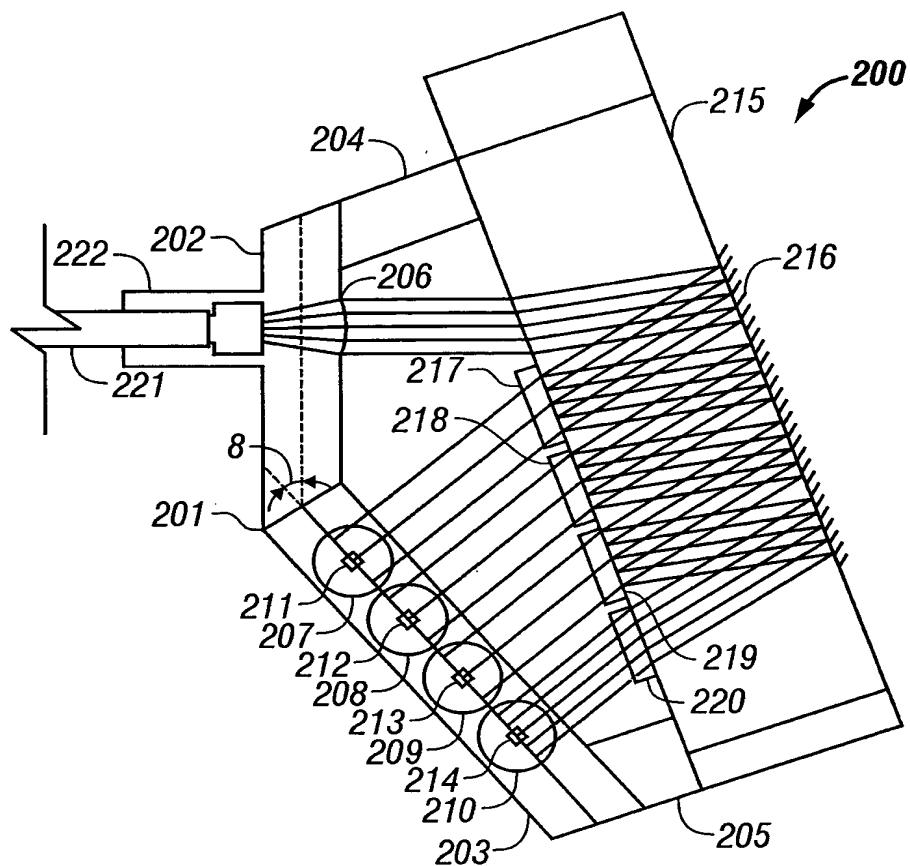


FIG. 2A

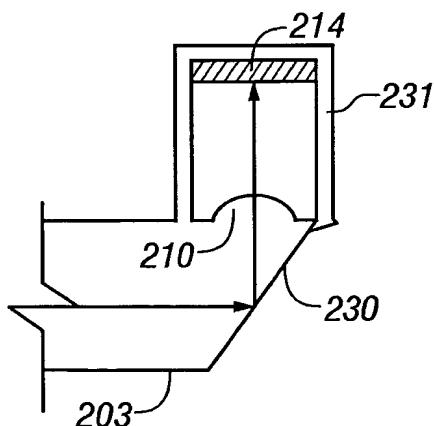


FIG. 2B

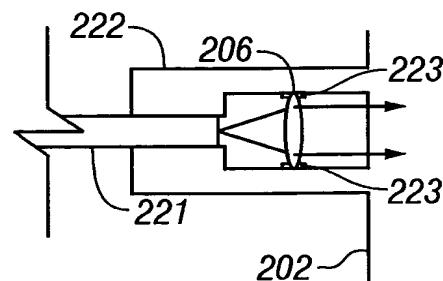


FIG. 2C

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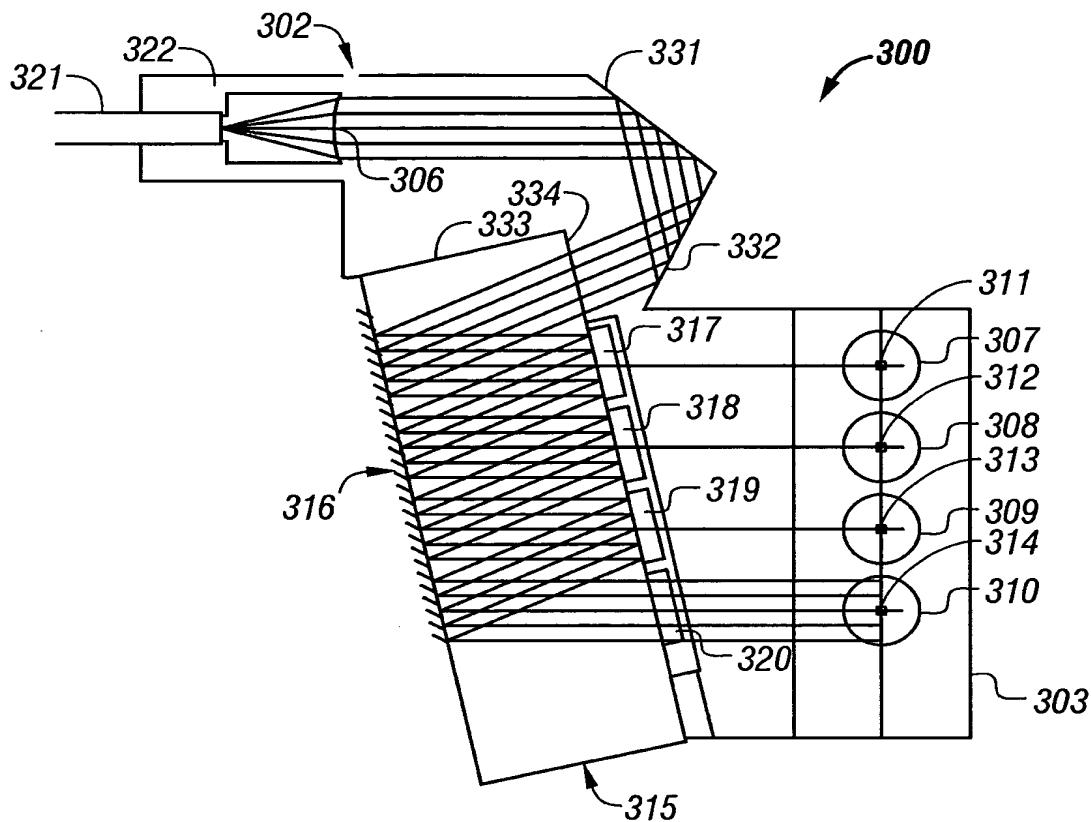


FIG. 3A

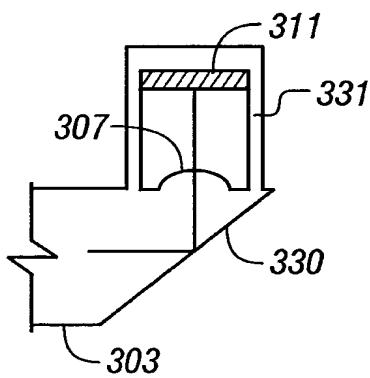


FIG. 3B

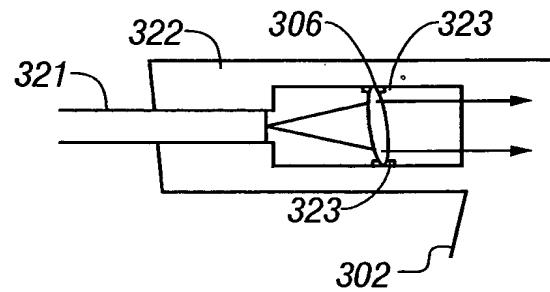


FIG. 3C

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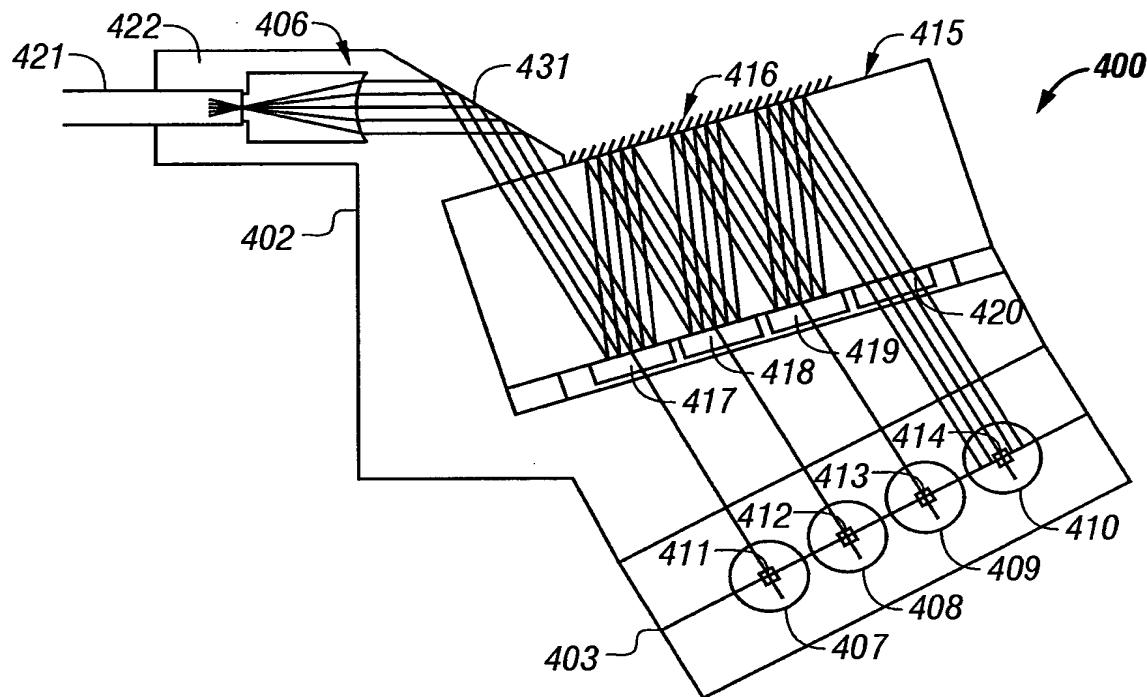


FIG. 4A

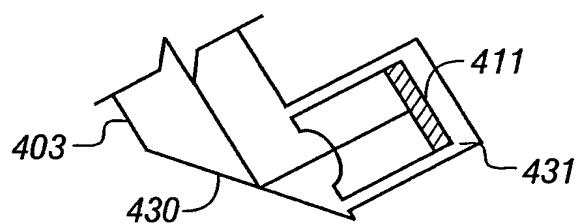


FIG. 4B

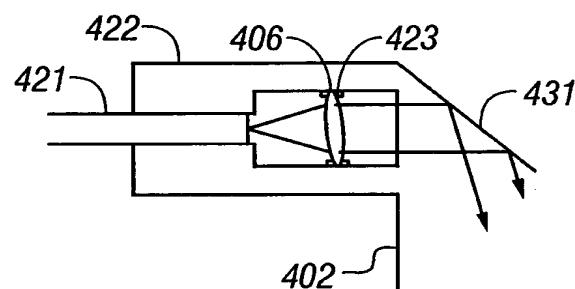


FIG. 4C

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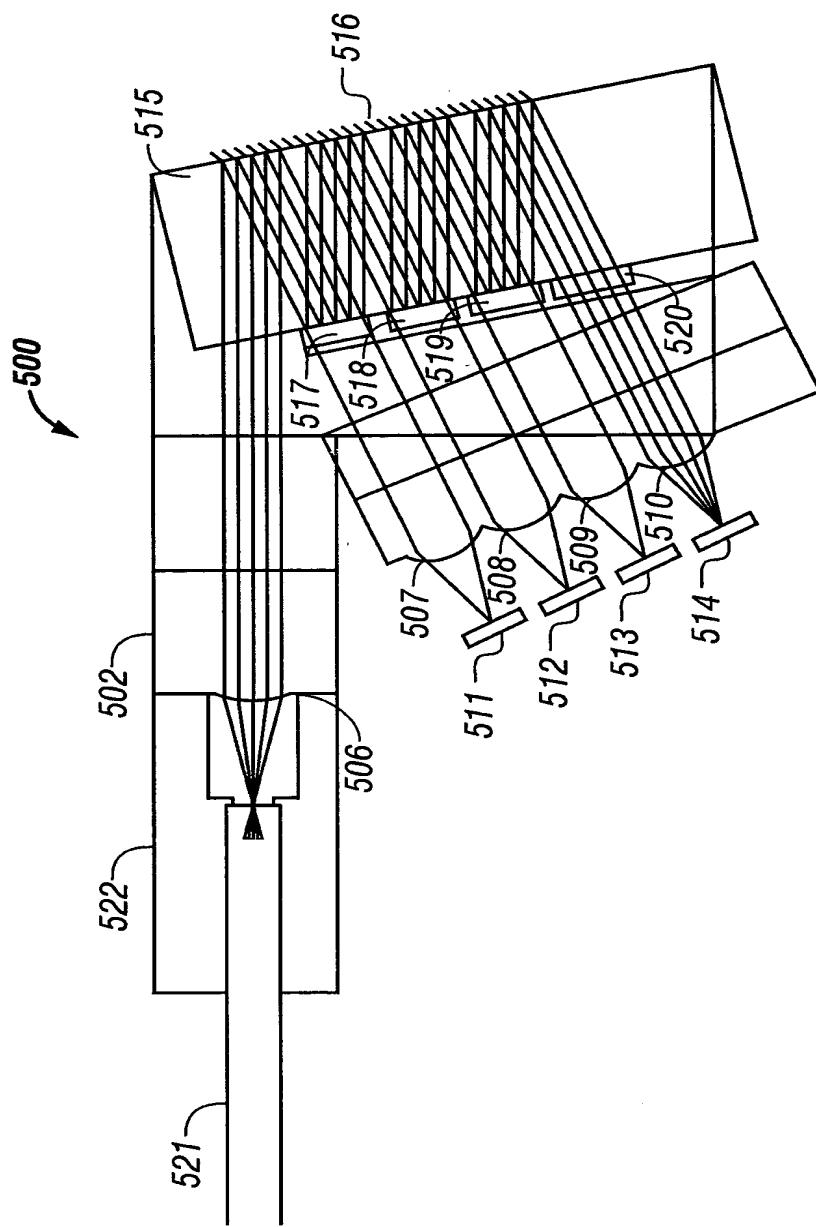


FIG. 5A

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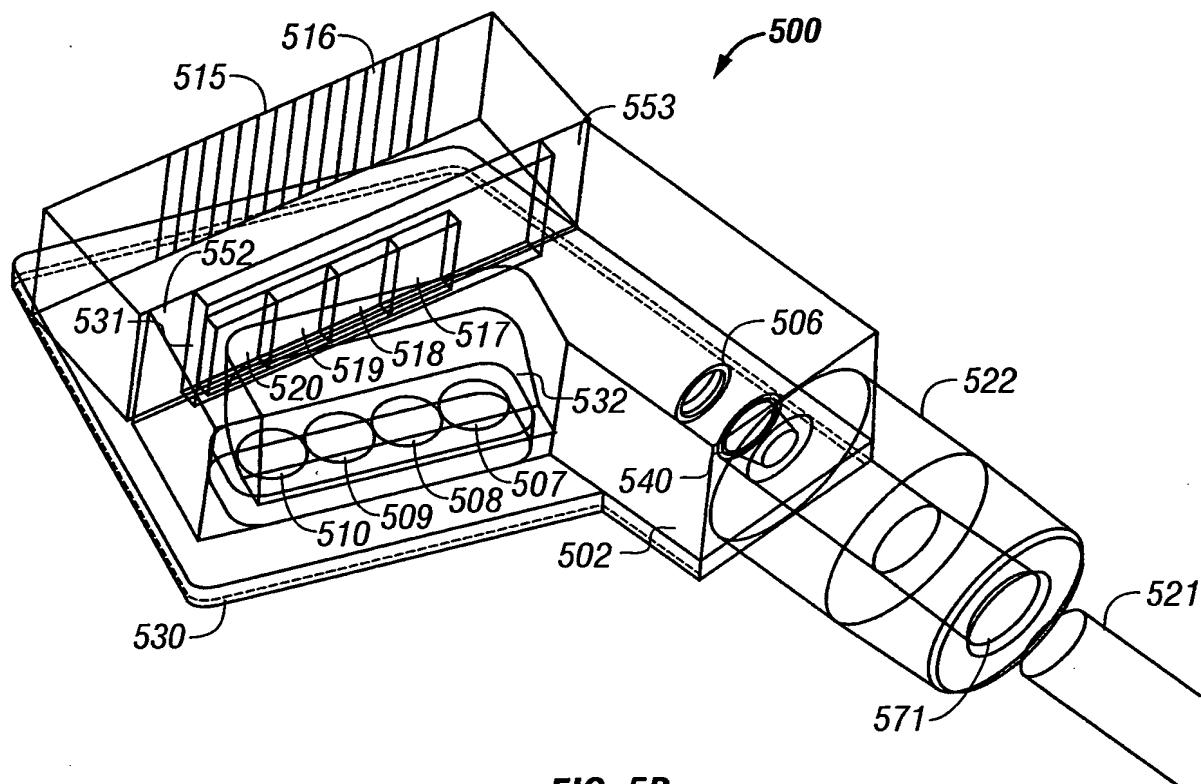


FIG. 5B

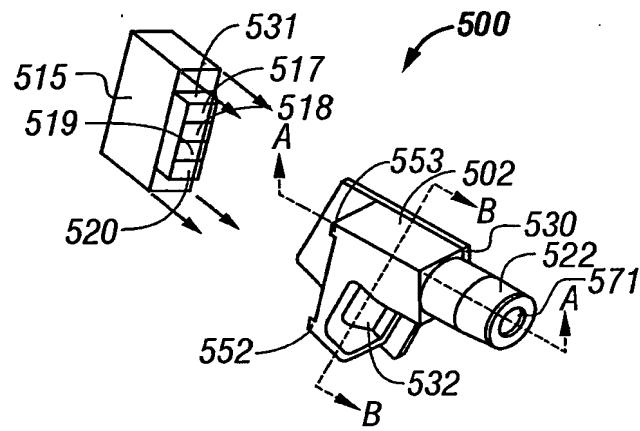


FIG. 5C

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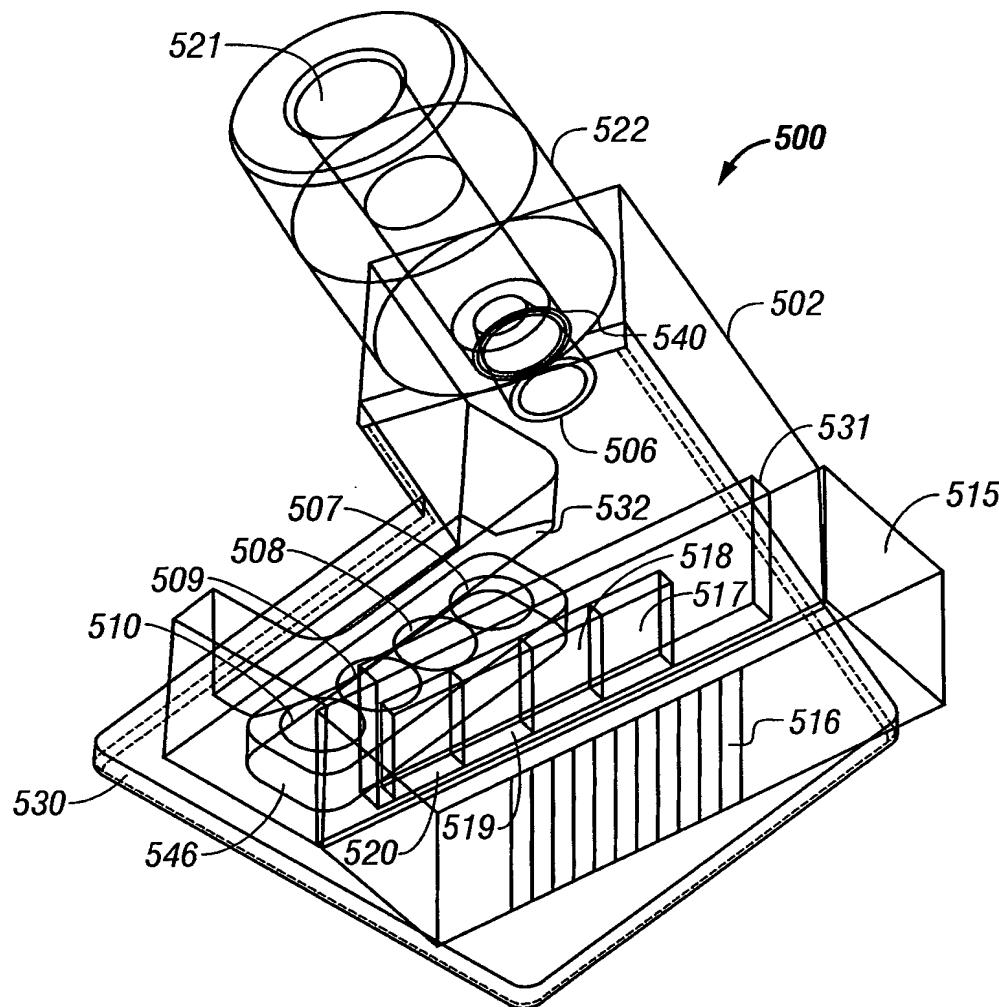


FIG. 5D

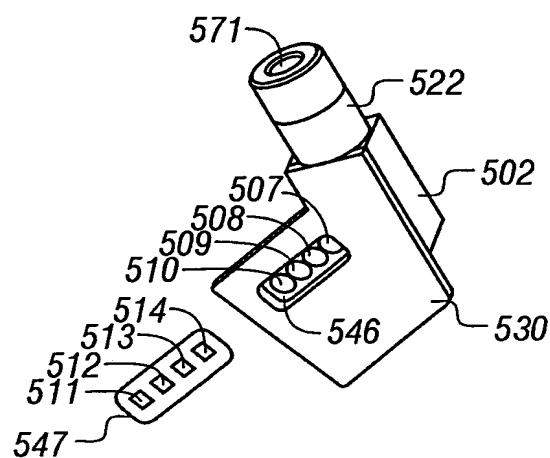


FIG. 5E

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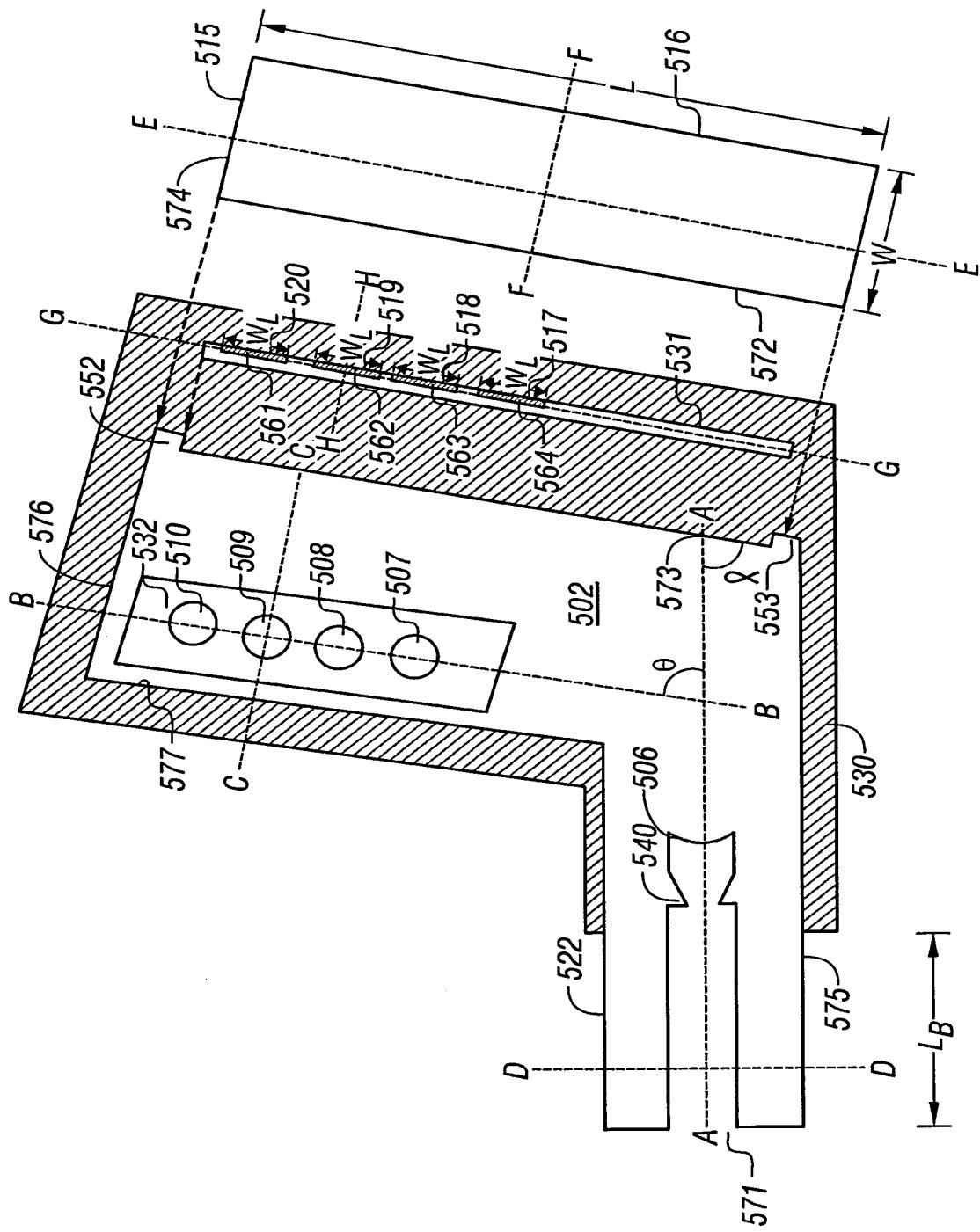


FIG. 5F

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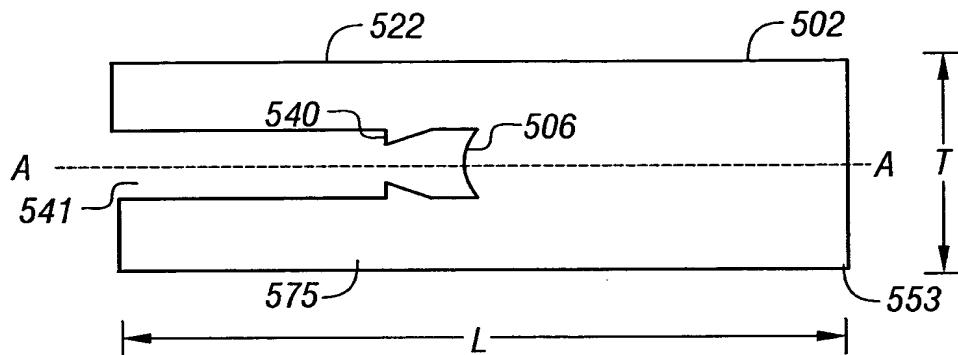


FIG. 5G

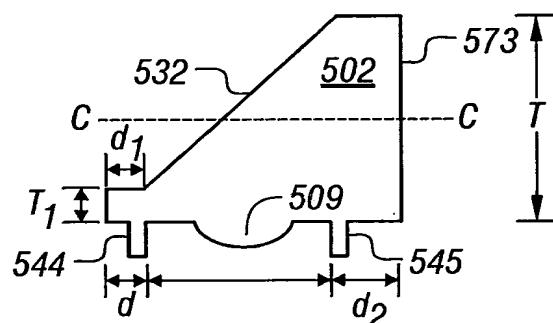


FIG. 5H

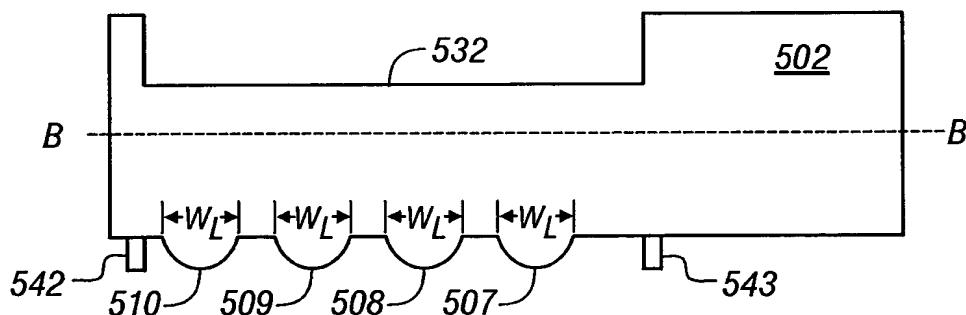


FIG. 5I

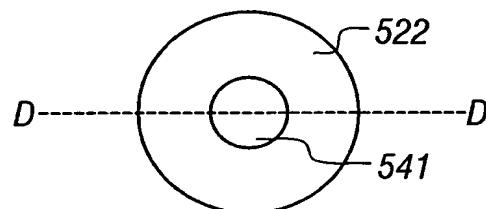


FIG. 5J

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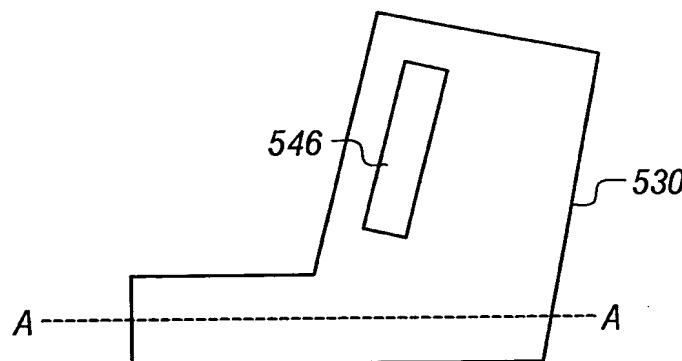


FIG. 5K

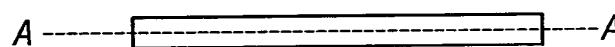


FIG. 5L

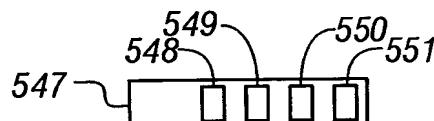


FIG. 5M

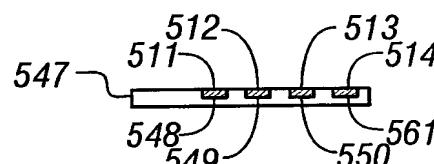


FIG. 5N

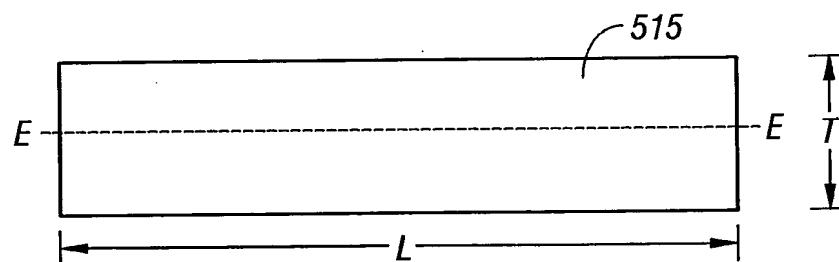


FIG. 5O

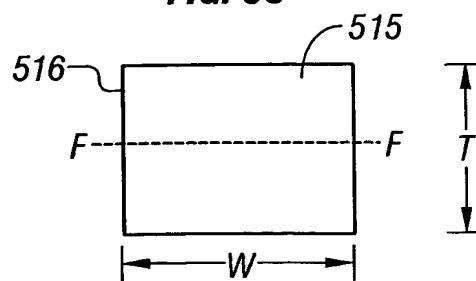


FIG. 5P

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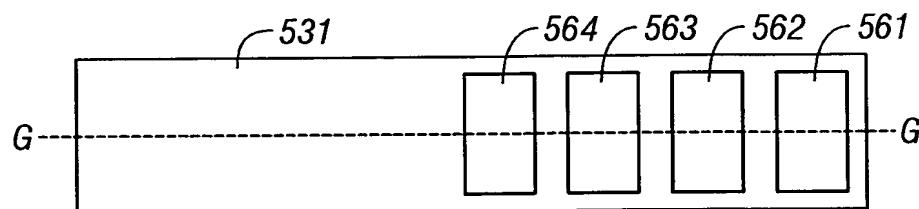


FIG. 5Q

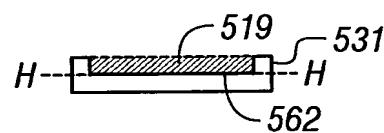


FIG. 5R

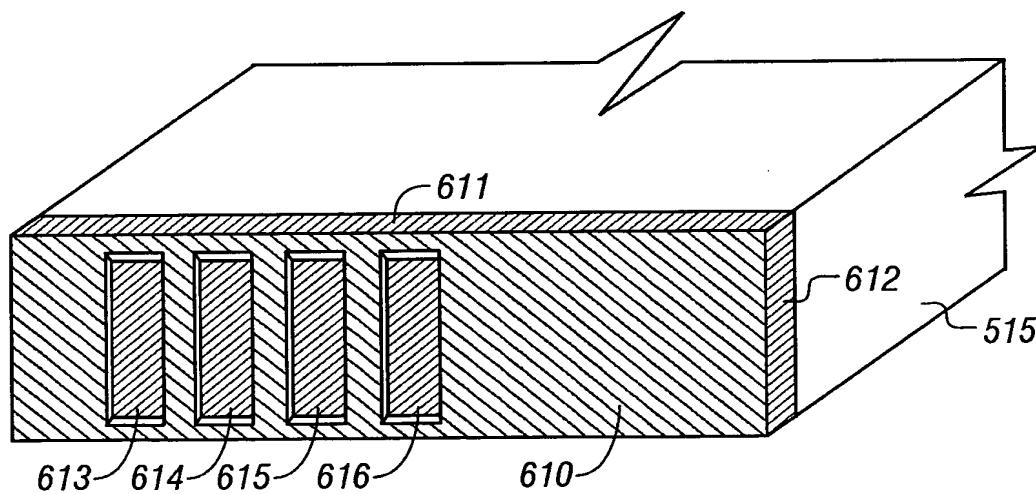


FIG. 6

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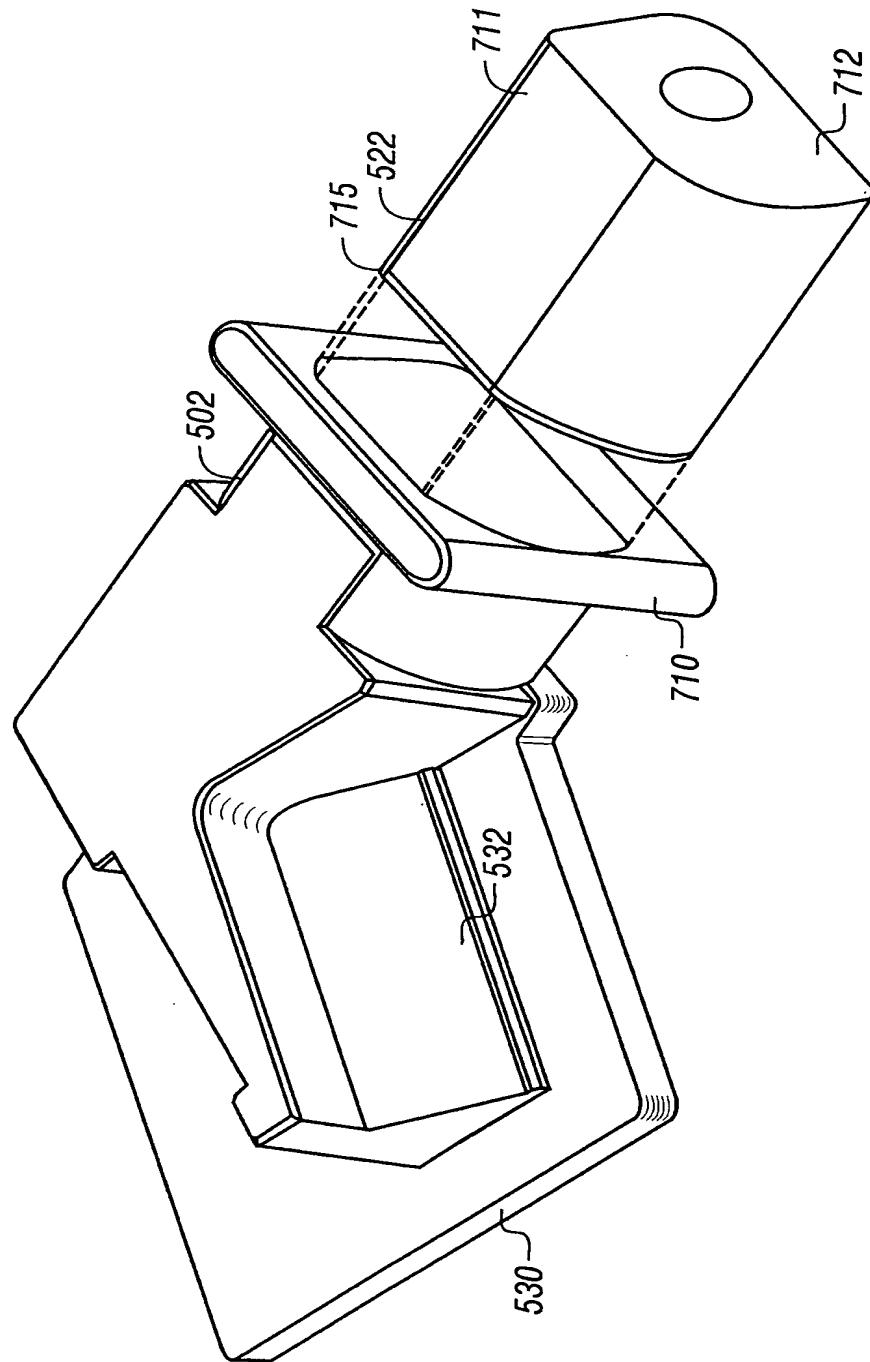


FIG. 7A

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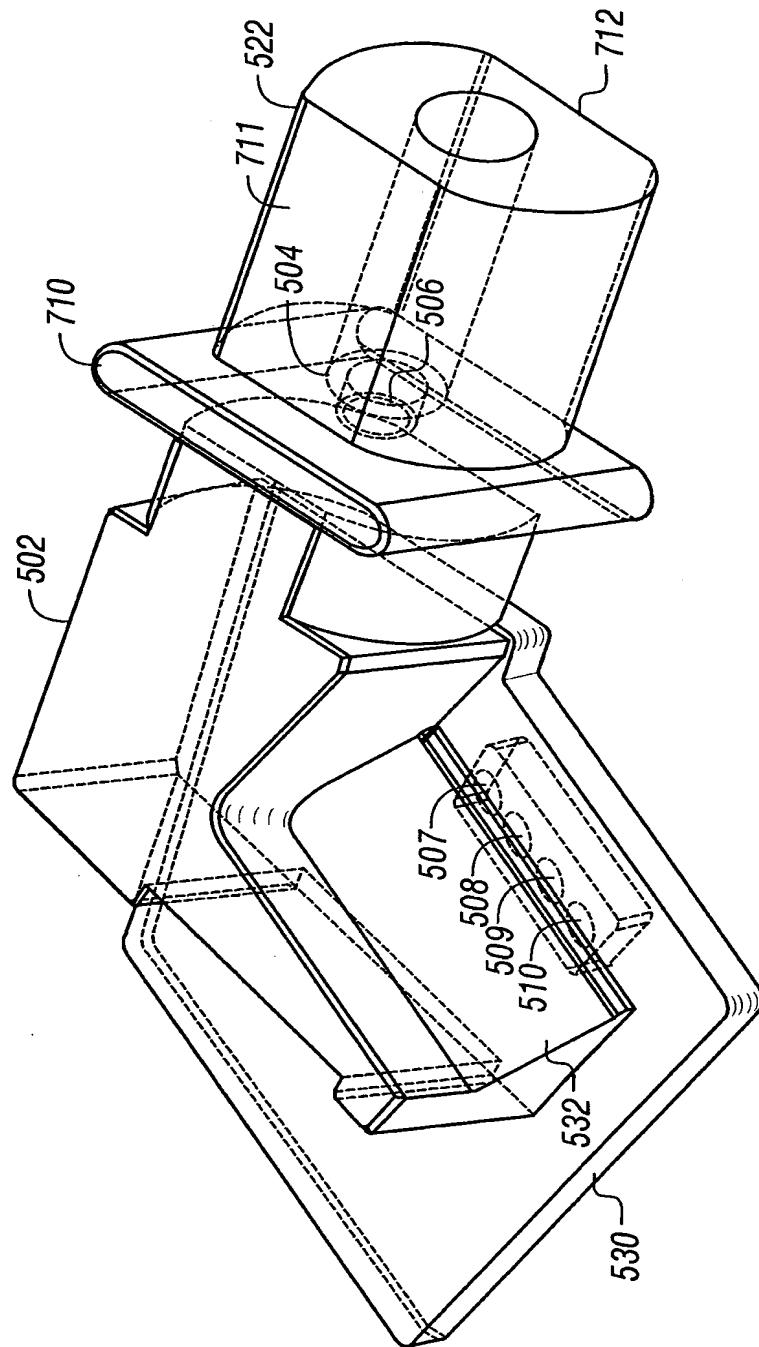
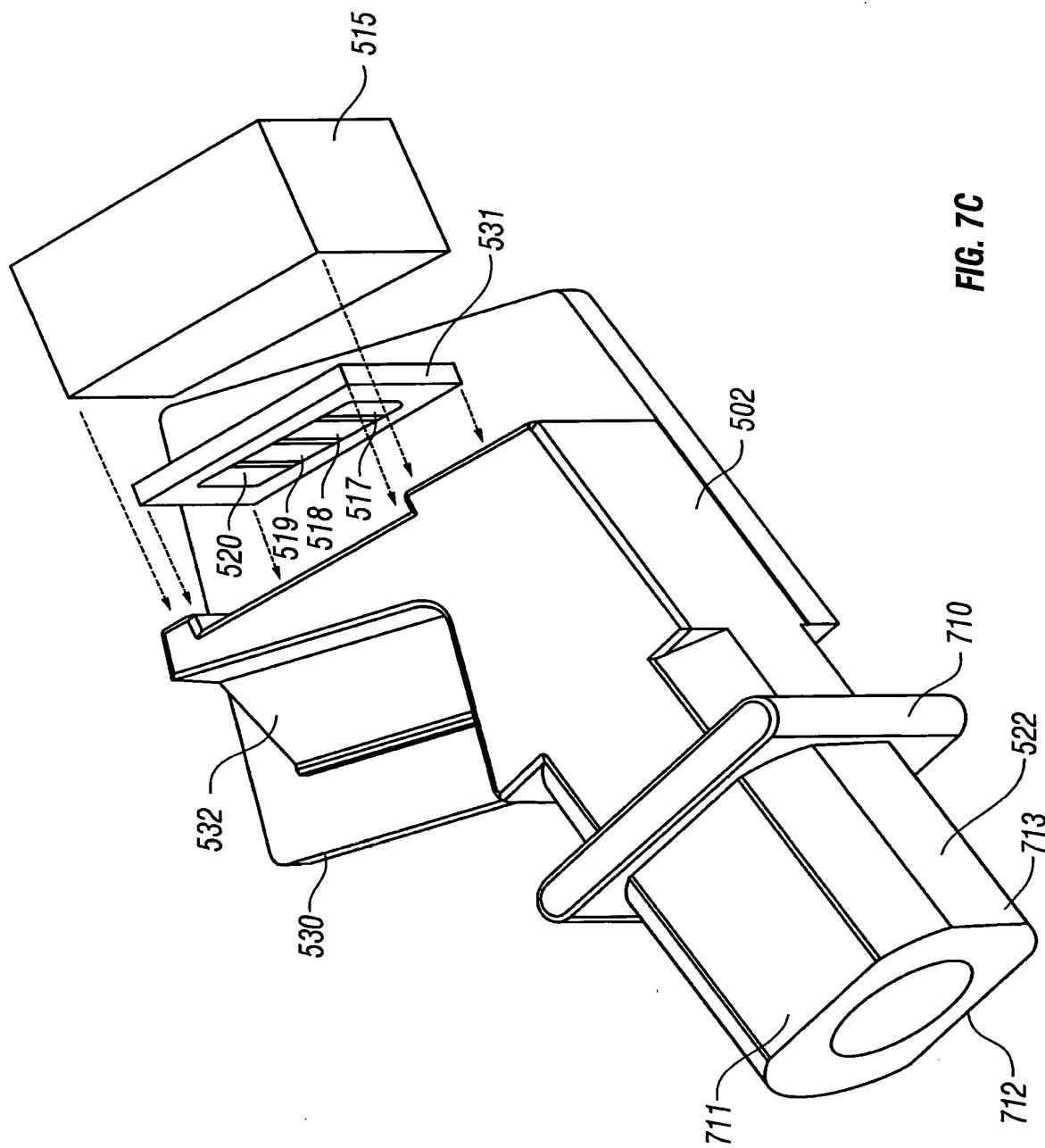


FIG. 7B

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